ABSTRACT OF DISCLOSURE

A method for a graphics chip to access data stored in a system memory of a
computer device is disclosed. The method includes using a memory controller to set a
5   block capacity value; using the memory controller to divide a plurality of read requests
corresponding to a predetermined request sequence into a plurality of request groups,
wherein a total amount of data required by read requests grouped in each request group is
less than the block capacity value; and using the memory controller to adjust a request
sequence corresponding to read requests grouped in each request group for retrieving data
10   stored at different N pages so that a memory device only performs N-1 page switching
operations.

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